

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		09989777
	Filing Date		2001-11-19
	First Named Inventor	Craig Nemecek	
	Art Unit	2123	
	Examiner Name	Mary C. Jacob	
	Attorney Docket Number	CD01208M	

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	1	5416895	B1	1995-05-16	Anderson, et al.	Entire Document
	2	6002398	B1	1999-12-14	Wilson, Daniel	Entire Document
	3	6014135	B1	2000-01-11	Fernandes, Antonio	Entire Document
	4	6052524	B1	2000-04-18	Pauna, Mark	Entire Document
	5	6104325	B1	2000-08-15	Liaw, et al.	Entire Document
	6	6121965	B1	2000-09-19	Kenney, et al.	Entire Document
	7	6223144	B1	2001-04-24	Barnett, et al.	Entire Document
	8	6282551	B1	2001-08-28	Anderson, et al.	Entire Document

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	9	6304101	B1	2001-10-16	Nishihara, Yoshio	Entire Document
	10	6384947	B1	2002-05-07	Ackerman, et al.	Entire Document
	11	6438565	B1	2002-08-20	Ammirato, et al.	Entire Document
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	20	5883623	B1	1999-03-16	Cseri, Istvan	Entire Document
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	27	5495593	B1	1996-02-27	Elmer, et al.	Entire Document
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	36	6438738	B1	2002-08-20	Elayda	Entire Document
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12	Hsieh et al., "Modeling Micro-Controller Peripherals for High-Level Co-Simulation and Synthesis," IEEE, 1997; 4 pages	<input type="checkbox"/>
13	Specks et al., "A Mixed Digital-Analog 16B Microcontroller with 0.5MB Flash Memory, On-Chip Power Supply, Physical Network Interface, and 40V I/O for Automotive Single-Chip Mechatronics," IEEE, Feb. 9, 2000; 1 page	<input type="checkbox"/>
14	"PSoC technology complete changes 8-bit MCU system design", Cypress Microsystems, Inc. retrieved from <http>://www.archive.org/web/20010219005250/http://cypressmicro.com-/t...>, Feb. 19, 2001; 21 pages	<input type="checkbox"/>

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15	"PSoC designer: Integrated development environment, getting started 25-minute tutorial, version 1.0", Cypress Microsystems., Cypress Microsystems, Inc. CMS10006A, July 3, 2001; 25 pages	<input type="checkbox"/>
16	ROBINSON, GORDON D; "Why 1149.1 (JTAG) Really Works", May 1994, Conference Proceedings Electro/94 International, 10-12 May 1994, Combined Volumes; 6 pages	<input type="checkbox"/>
17	Monte Mar, Bert Sullam, Eric Blom; "An architecture for a configurable Mixed-signal device", 2003, IEEE Journal of Solid-State Circuits, Volume 3; 4 pages	<input type="checkbox"/>
18	Julio Faura et al.; "A NOVEL MIXED SIGNAL PROGRAMMABLE DEVICE WITH ON-CHIP MICROPROCESSOR", 1997, IEEE 1997 Custom Integrated Circuits Conference; 4 pages	<input type="checkbox"/>
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28	ITO, SERGIO AKIRA AND CARRO, LUIGI; "A Comparison of Microcontrollers Targeted to FPGA-Based Embedded Applications", September 2000, Proceedings of 13th Symposium on Integrated Circuits and Systems Design, 18-24 September 2000; 6 pages	<input type="checkbox"/>

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